

Amendments to the Drawings:

Please delete page 8/8 of the drawings, labeled as "Reference marks in the drawings" in its entirety.

Amendment to the Abstract:

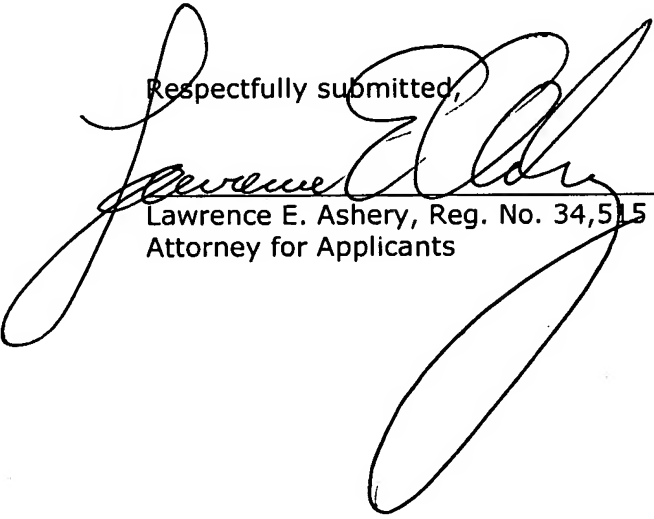
The Abstract has been amended. A revised Abstract is attached.

ABSTRACT

An image signal processing device has an LSI ~~(21)~~ including a ROM access control circuit ~~(22)~~ for holding data for controlling an operation of a video signal processing unit, and a flush ROM ~~(23)~~ that is disposed outside the LSI ~~(21)~~, holds control data to be transmitted to the ROM access control circuit ~~(22)~~, and allows data read to be controlled by the ROM access control circuit ~~(22)~~. The data transferred between the flush ROM ~~(23)~~ and the ROM access control circuit ~~(22)~~ has data that must be updated every field and data that does not need to be updated every field. The data is transferred in a vertical blanking time period of the video output data. This configuration allows certain transfer of required data using the vertical blanking time period.

Attachment

Respectfully submitted,



 Lawrence E. Ashery, Reg. No. 34,515
 Attorney for Applicants

LEA/ds

Attachments: Abstract

Dated: January 5, 2006

P.O. Box 980
 Valley Forge, PA 19482
 (610) 407-0700

EXPRESS MAIL


Mailing Label Number:

EV 497722835 US

Date of Deposit:

January 5, 2006

I hereby certify that this paper and fee are being deposited, under 37 C.F.R. § 1.10 and with sufficient postage, using the "Express Mail Post Office to Addressee" service of the United States Postal Service on the date indicated above and that the deposit is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



 Kathleen Libby